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August 4, 2008

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Art Unit 2836

Attn: Mail Stop Amendment

Re: U.S. Utility Patent Application
Application No. 10/668,249; Filed: September 24, 2003
For: System and Method to Relieve ESD Requirements of NMOS Transistors
Inventor: Hongwei Wang
Our Ref: 1875.5090000

Sir:

Transmitted herewith for appropriate action is the following document:

1. Reply Under 37 C.F.R. § 1.111.

The above-listed document is filed electronically through EFS-Web.

In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

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Enclosure(s)

855228 1.DOC

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